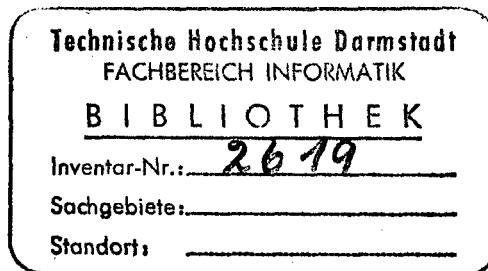


THE LOGICAL DESIGN OF OPERATING SYSTEMS

ALAN C. SHAW

*Computer Science Group
University of Washington*



PRENTICE-HALL, INC.

ENGLEWOOD CLIFFS, N. J.

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